



Dkt. 2271/60617

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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TECHNOLOGY CENTER 2800
#22/D
6/24/03
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Application of : Yoshinori UEDA

Serial No. : 09/431,593

Group Art Unit: 2815

Date Filed : November 1, 1999

Examiner: M. Warren

For : SEMICONDUCTOR DEVICE HAVING AN
INTEGRAL RESISTANCE ELEMENT

1185 Avenue of the Americas
New York, N.Y. 10036

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to examination on the merits, please amend the above-identified application as follows:

In the Claims:

Please amend claim 1 as follows:

DI SUB E3
1. (Thrice Amended)

A semiconductor device, comprising:

a Si substrate; and

a resistance element formed on said Si substrate,

I hereby certify that this paper is being deposited this date with the
U.S. Postal Service as first class mail addressed to Assistant
Commissioner for Patents, Washington, D.C. 20231.

Richard F. Jaworski
Reg. No. 33,515

June 11, 2003
Date